

1 **IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

2 Application Serial No. 08/530,661
3 Filing Date September 20, 1995
4 Inventor Brent Keeth
5 Assignee Micron Technology, Inc.
6 Group Art Unit 2503
7 Examiner N. Kelly
8 Attorney's Docket No. MI22-356
9 Title: Semiconductor Memory Circuit

10 **RESPONSE TO DECEMBER 23, 1996 OFFICE ACTION**

11 To: BOX NON-FEE AMENDMENT
12 Assistant Commissioner For Patents
13 Washington, D.C. 20231

14 From: David G. Latwesen (Tel. 509-624-4276; Fax 509-838-3424)
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18 **REMARKS**

19 Applicant elects to prosecute the species of Group II (a 16M
20 semiconductor memory device). The claims readable on Group II are
21 claims 6-10, 18-19, 22-23 and 25-26. Claims 22 and 25 are generic to
22 a 4M, 16M or 64M semiconductor memory device. Applicant requests
23 examination of the claims in the application on the merits.

24 Respectfully submitted,

25 Dated: _____

26 By: _____
27 David G. Latwesen, Ph.D.
28 Reg. No. 38,533